

U.S. Application No. 10/750,883
By: Naoshi ADACHI et al.
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Title: Heat Treatment Jig for
Semiconductor Substrate and Method...
REPLACEMENT SHEET

FIG.8

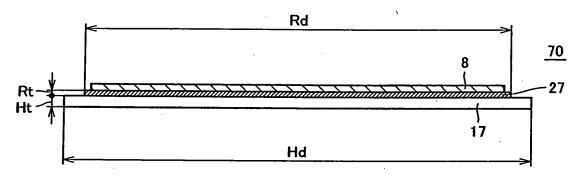


FIG.9

